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Computer Systems Design Laboratory

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CSC 34300 – 1XY: Computer Systems Design Laboratory

Office Hours: Tuesday & Thursday 6:00pm – 9:20pm
Office Location: Baskervill 106

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- **Textbook**: NO TEXTBOOK REQUIRED

- **Recommended Resources**:
  - FPGA Prototyping by VHDL Examples: Xilinx Spartan-3 Version, *by Pong P. Chu*

- **Corequisites**: CSC 34200.

- **Software**: MIPS Simulator and VHDL Simulator

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